

SEP 22 2005

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. .... 09/388,857  
Filing Date ..... September 1, 1999  
Inventor ..... Luan C. Tran  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... Laura M. Schillinger  
Attorney Docket No. .... MI22-0878  
Customer No.: ..... 021567  
Title: Semiconductor Processing Methods of Forming Transistors, Semiconductor  
Processing Methods of Forming Dynamic Random Access Memory  
Circuitry, and Related Integrated Circuitry

Assistant Commissioner for Patents  
Washington, D.C. 20231

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Dated: 22 Sept 2005

By: Rhonda G. Rambo  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE		
In re application of Luan C. Tan		
Application No.: 09/388,857	Group No.: 2813	
Filed: 09/01/99	Examiner: L. Schilling	
For: Semiconductor Processing Methods of Forming Transistors, Semiconductor Processing Methods of Forming Dynamic Random Access Memory Circuitry, and Related Integrated Circuitry		
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Practitioner's Docket No. MI22-878

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Luan C. Tran

Application No.: 09/388,857  
Filed: 09/01/99

Group No.: 2813  
Examiner: L. Schillinger

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For: Semiconductor Processing Methods of Forming Transistors, Semiconductor Processing Methods of Forming Dynamic Random Access Memory Circuitry, and Related Integrated Circuitry

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SEP 22 2005

Application Serial No. .... 09/388,857  
Filing Date ..... September 1, 1999  
Inventor ..... Luan C. Tran  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... L. Schillinger  
Attorney's Docket No. .... MI22-878  
Customer No. .... 021567

Title: Semiconductor Processing Methods of Forming Transistors, Semiconductor Processing Methods of Forming Dynamic Random Access Memory Circuitry, and Related Integrated Circuitry

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References - - See attached Form PTO-1449

The attached Form PTO-1449 is submitted in compliance with 37 C.F.R. §§ 1.56. Pursuant to Federal Register Vol. 69, No. 182, pg. 56542 (September 21, 2004), no copies of any cited U.S. patents or U.S. published applications are included herewith. Copies of all other cited art references are attached. No admission is made regarding whether all the submitted references are prior art.

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Respectfully submitted,

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LIST OF ART CITED BY APPLICANT <small>(Use several sheets if necessary.)</small>				APPLICANT Luan C. Tran			
				FILING DATE September 1, 1999		GROUP 2813	
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,834,019	12/04	Tran et al.			
	AB	6,734,487	05/04	Tran et al.			
	AC	6,228,713	05/01	Pradeep et al.			
	AD	6,501,114	12/02	Cho et al.			
	AE	6,620,704	09/03	Miura et al.			
	AF	6,436,751	08/02	Liou et al.			
	AG	6,770,927	08/04	Cho et al.			
	AH						
	AI	2004/0188777A1	09/04	Hwang			
	AJ	2004/0214355A1	10/04	Miura et al.			
	AK						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
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SEMICONDUCTOR PROCESSING METHODS OF FORMING T  
PROCESSING METHODS OF FORMING DYNAMIC RANDOM  
RELATED INTEGRATED CIRCUITRYTransaction  
History

Date

Contents Description

07-19-2005	Date Forwarded to Examiner
07-08-2005	Response after Non-Final Action
07-08-2005	Request for Extension of Time - Granted
03-18-2005	Reference capture on IDS
03-18-2005	Information Disclosure Statement (IDS) Filed
03-08-2005	Mail Non-Final Rejection
03-07-2005	Non-Final Rejection
02-10-2005	Information Disclosure Statement (IDS) Filed
08-23-2004	Workflow - Request for RCE - Begin
01-25-2005	IFW TSS Processing by Tech Center Complete
08-23-2004	Reference capture on IDS
08-23-2004	Information Disclosure Statement (IDS) Filed
01-25-2005	Date Forwarded to Examiner
01-25-2005	Date Forwarded to Examiner
08-23-2004	Request for Continued Examination (RCE)
01-25-2005	DISPOSAL FOR A RCE/CPA/129 (express aba
08-23-2004	Request for Extension of Time - Granted
01-03-2005	Workflow incoming amendment IFW
03-18-2004	Reference capture on IDS
03-18-2004	Information Disclosure Statement (IDS) Filed
03-23-2004	Mail Final Rejection (PTOL - 326)
03-22-2004	Final Rejection
01-07-2004	Date Forwarded to Examiner
12-05-2003	Response after Non-Final Action
12-05-2003	Incoming Letter Pertaining to the Drawings
12-05-2003	Request for Extension of Time - Granted
08-05-2003	Mail Non-Final Rejection

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